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BASIC MICROPROCESSOR- ORDER CODE SUMMARY

0 N IDLE	D N N → P
1 N R(N)+1	E N N → X
2 N R(N)-1	F 0 M(R(X)) → D
4 N M(R(N)) → D, R(N)+1	F 1 M(R(X))/D → D
5 N D → M(R(N))	F 2 M(R(X)) & D → D
8 N RO(N) → D	F 3 M(R(X)) ⊕ D → D
9 N R1(N) → D	F 4 M(R(X)) plus D → D
A N D → RO(N)	F 5 M(R(X)) minus D → D
B N D → R1(N)	F 6 SHIFT D RIGHT ONE BIT
C N DO → ROO(N)	F 7 SPARE
6 3 RESET EF3 & EF4, R(X)+1	6 8 INPUT BYTE → M(R(X))
7 8 T → M(R(X))	7 0 M(R(X)) → XP, R(X)+1, RESET IM
3 0 Y Y → RO(P) UNCONDITIONAL BRANCH	
3 1 Y Y → RO(P) IF D ≠ 0	
3 2 Y Y → RO(P) IF D = 0	
3 3 Y Y → RO(P) IF DF = 1	
3 4 Y Y → RO(P) IF EF1 = 1 (INPUT BYTE READY)	
3 5 Y Y → RO(P) IF EF2 = 1	
3 6 Y Y → RO(P) IF EF3 = 1 (YES SWITCH)	
3 7 Y Y → RO(P) IF EF4 = 1 (NO SWITCH)	
6 1 & M(R(X)) = 0 1 SELECT INPUT (CARD/SWITCH), R(X)+1	
6 2 & M(R(X)) = 0 1 SET SELECT INPUT TO PROGRAM MODE, R(X)+1	
6 2 & M(R(X)) = 0 2 SET SELECT INPUT TO DIRECT MODE, R(X)+1	
6 1 & M(R(X)) = 0 2 SELECT OUTPUT (DISPLAY), R(X)+1	
6 2 & M(R(X)) = 0 1 SET SELECT OUTPUT TO 32x32 MODE, R(X)+1	
6 2 & M(R(X)) = 0 2 SET SELECT OUTPUT TO 16x64 MODE, R(X)+1	

FIGURE 6

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