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Title
 1802 Development Card

Size
 C:\ORCAD\PROJECT\1802.DRV.SCH

REV
 A

Date: February 22, 2005 | Sheet 1 of 1

U2a pinouts

23	26	27	Jumpers at J2
62256 32K RAM	A11	A3	1-3-4-5-7-9-10
6264 8K RAM	A11	VCC	4-6-5-7-8-10
6116 2K RAM	VCC	/WE	5-6-8-10
27328 16K ROM	VCC	A14	1-3-4-5-7-9-10
2764 8K ROM	VCC	A11	1-3-4-5-7-9-10
2732 4K ROM	VCC	A11	1-3-4-5-7-9-10
2716 2K ROM	VCC	VCC	3-5-8-10

PORT INSTALL JUMPERS AT J1

1	1-3-3-5-4-6-7-9-8-10
2	1-2-3-4-5-6-7-9-8-10
3	1-2-3-7-5-6-7-9-8-10
5	1-2-3-5-4-7-6-9-8-10
6	1-3-2-4-5-6-5-7-9-10
7	1-2-1-3-5-6-5-7-9-10

Used: C1-3, D0-9, J1-5, R1-19, S0-11, U1-10.
 R7-R19 can be discrete, SIP, or DIP package.
 Install EITHER U2a-U3a OR U2b-U3b.
 IC power supply pins not shown.