



Notes:

- Used: C1-5, D0-10, J1-2, R1-10, S0-11, U1-9.
- The two boards stack together; CPU on bottom, I/O board on top, both component side up.
- J2 mounts on edge of I/O board.
- P1/J1 are a mating pair of 0.025" pins on 0.1" centers.
- IC power supply pins not shown.

U2 Jumper:

1	23	26	27	Jumpers at P2		
62256	8K RAM	A11	A13	/WE	1-2,4-6,5-7,9-10	
6264	8K RAM	A11	VCC	/WE	4-6,5-7,8-10	
6116	2K RAM	VCC	VCC		5-6,8-10	
27256	32K RAM	VCC	A11	A13	1-3,2-4,5-7,9-10	
27128	16K RAM	VCC	A11	A13	1-3,3-4,5-7,9-10	
2764	8K ROM	VCC	A11	NC	VCC	1-3,3-4,5-7
2732	4K ROM	VCC	A11	VCC	5-7,8-10	
2715	2K ROM	VCC	VCC		3-5,7,8-10	

PC PARALLEL:

1	OVCC
2	OVCC
3	OVCC
4	OVCC
5	OVCC
6	OVCC
7	OVCC
8	OVCC
9	OVCC
10	OVCC
11	OVCC
12	OVCC
13	OVCC
14	OVCC
15	OVCC
16	OVCC
17	OVCC
18	OVCC
19	OVCC
20	OVCC
21	OVCC
22	OVCC
23	OVCC
24	OVCC
25	OVCC

Size/Document Number: 1802 ElfStamp Card (1 and 2 of 2)

Date: October 8, 2005

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